

Serial No.: 09/540,591
Examiner: Hsu, Alpus

In the Specification:

Please replace the paragraph beginning at ~~page 1, line 6~~, through ~~page 2, line 6~~, with the following rewritten paragraph:

C2
This application discloses subject matter related to the subject matter disclosed in the following co-assigned patent applications: (1) "Method and Apparatus for Routing Alarm Signals in a Signaling Server," filed 12/13/2001 Ser. No. 10/020,619 (~~Attorney Docket Number: 1285-0007~~), in the name(s) of: Val Teodorescu; (2) "Card Design Having Tape and Disk Drives," filed 3/31/2000 Ser. No. 09/539,759 now US Patent No. 6,636,917 issued 10/21/2003 (~~Attorney Docket Number: 1285-0004~~), in the name(s) of: Ignacio Linares and Serge Fourcand; (3) "Clock Distribution Scheme in a Signaling Server," filed 3/31/2000, Ser. No. 09/541,002 now U.S. Patent No. 6,643,791 issued 11/4/2003 (~~Attorney Docket Number: 1285-0008~~), in the name(s) of: Val Teodorescu; and (4) "Bus Control Module with System Slot Functionality in a Compact Peripheral Component Interconnect Bus System," filed 3/31/2000, Ser. No. 09/540,594 now abandoned (~~Attorney Docket Number: 1285-0006~~), in the name(s) of: Serge Fourcand, Curt McKinley, and Val Teodorescu.

Please replace the paragraph beginning at ~~page 37, line 5~~, through ~~line 16~~, with the following rewritten paragraph:

C3
Further details regarding the architecture of the multi-stage clock/alarm distribution system, level assignment and identification of the CDTMs and BCTMs using the framed SFI signal, and the exemplary signaling protocols used for the Time-Division Multiplexed SFI and EAS signals are provided in the following co-pending commonly owned patent applications which have been cross-referenced hereinabove and are incorporated by reference herein: (1) "Method and Apparatus for Routing Alarm Signals in a Signaling Server," filed 12/13/2001 Ser. No. 10/020,619 (~~Attorney Docket~~

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~~Number: 1285-0007~~, in the name(s) of: Val Teodorescu; and (2) "Clock Distribution Scheme in a Signaling Server," filed 3/31/2000, Ser. No. 09/541,002 now U.S. Patent No. 6,643,791 issued 11/4/2003 (~~Attorney Docket Number: 1285-0008~~), in the name(s) of: Val Teodorescu.

Please replace the paragraph beginning at page 39, line 17, through page 40, line 7, with the following:

C4
A plurality of incoming reference clock signals 336 derived from the network signals at the line interface cards are provided to a selector 318 in the BCTM 116. Based on the encoded data in the SFI signal 214 which is demuxed by the demultiplexer 316, an appropriate derived clock is selected to be provided as the reference clock signal 218 to the R-Level CDTMs on both A- and B-sides of the distribution system. Additional details regarding the reference clock selection process in the multi-stage distribution system are provided in the following co-pending commonly assigned patent application which has been incorporated by reference hereinabove: "Clock Distribution Scheme in a Signaling Server," filed 3/31/2000, Ser. No. 09/541,002 now U.S. Patent No. 6,643,791 issued 11/4/2003 (~~Attorney Docket Number: 1285-0008~~), in the name(s) of: Val Teodorescu.

Please replace the paragraph beginning at page 45, line 5, through line 20, with the following:

C5
The FPGA block 352 is also provided with various alarm inputs 340 and status information from the PBAs which are multiplexed into the serial Status signal 216. Preferably, the BCTM uses the rack ID fields within the SFI to determine the appropriate frame periods in which it should drive the status indicators. Also, the PBA code for the selected reference clock derived from the network signal(s) is inserted in to suitable fields of the Status signal. The alarm inputs 340 preferably include PSU, breaker panel, and fan alarms, among others. In addition, power status of the Ethernet switches is

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also monitored and appropriately reported. Further details regarding the EAS signal construction and the alarm/status encoding thereof may be found in the following co-pending commonly assigned patent application which has been incorporated by reference hereinabove: "Method and Apparatus for Routing Alarm Signals in a Signaling Server," filed 12/13/2001 Ser. No. 10/020,619 (~~Attorney Docket Number: 1285-0007~~), in the name(s) of: Val Teodorescu.

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